

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7664	"level shifter"	US-PGPUB; USPAT	OR	OFF	2006/01/31 09:21
L2	29	1 and (passgate or transfergate)	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:39
L3	408	1 and ((pass or transfer or transmission) adj (transistor\$1 or switch))	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:40
L4	97	(2 or 3) and "327"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2006/01/31 09:23
L5	62	(2 or 3) and "326"/\$.ccls.	US-PGPUB; USPAT	OR	OFF	2006/01/31 09:23
L6	24	4 and 5	US-PGPUB; USPAT	OR	OFF	2006/01/31 09:28
L7	73	4 not 6	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:22
L8	38	5 not 6	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:24
L9	101	3 and ("AND gate" or "AND logic" or "logic AND") not (4 or 5)	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:56
L10	177	3 not (4 or 5 or 9)	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:29
L11	174	10 and (transistor\$1 or PMOS or NMOS or MOS or MOSFET or FET)	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:29
L12	106	11 and capacitor\$1	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:32
L13	68	11 not 12	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:34
L14	2194	327/112,333,380,389,391,437.ccls.	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:34
L15	1485	326/68-71,121.ccls.	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:35
L16	328	(14 or 15) and ("AND gate" or "AND logic" or "logic AND")	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:35
L17	1297	(14 or 15) and capacitor\$1	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:35
L18	1450	(14 or 15) and shift\$3	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:36
L19	53	16 and 17 and 18	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:30
L20	28	(14 or 15) and (passgate or transfergate or throughgate)	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:39
L21	1387	(14 or 15) and ((pass or transfer or transmission or through) adj (transistor\$1 or switch or gate))	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:29

L22	167	16 and (20 or 21)	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:40
L23	133	22 not (4 or 5 or 19)	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:54
L24	138	16 not (4 or 5 or 9 or 19 or 23)	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:58
L25	562	(20 or 21) and 17	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:58
L26	492	25 not (4 or 5 or 9 or 19 or 23 or 23)	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:31
L27	196	26 and 18	US-PGPUB; USPAT	OR	OFF	2006/01/31 10:59
L28	407	26 and gate and drain and source	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:32
L29	162	27 and 28	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:00
L30	342	28 and (PMOS or (P adj (MOS or channel or type)))	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:33
L31	360	28 and (NMOS or (N adj (MOS or channel or type)))	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:33
L32	132	29 and 30 and 31	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:14
L33	30	29 not 32	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:12
L34	202	30 and 31 not 29	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:14
L35	0	34 and shift\$3	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:14
L36	92	34 and buffer	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:19
L37	110	34 not 36	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:22
L38	19	(30 or 31) not (29 or 34)	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:23
L39	24	28 not (29 or 30 or 31)	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:26
L40	34	27 not 28	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:27
L41	51	26 not (27 or 28)	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:27
L42	539	17 and 18	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:30
L43	277	42 not (4 or 5 or 9 or 19 or 23 or 24 or 26)	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:47
L44	277	43 and capacitor\$1	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:32

L45	212	44 and gate and drain and source	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:48
L46	207	44 and (NMOS or (N adj (MOS or channel or type)))	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:48
L47	204	44 and (PMOS or (P adj (MOS or channel or type)))	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:33
L48	172	45 and 46 and 47	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:42
L49	114	48 and logic	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:37
L50	58	48 not 49	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:37
L51	69	(45 or 46 or 47) not 48	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:43
L52	692	(20 or 21) not (4 or 5 or 9 or 19 or 23 or 24 or 26 or 43)	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:48
L53	575	52 and gate and drain and source	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:48
L54	632	52 and (NMOS or (N adj (MOS or channel or type)))	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:49
L55	618	52 and (PMOS or (P adj (MOS or channel or type)))	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:49
L56	530	53 and 54 and 55	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:49
L57	0	56 and capacitor\$1	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:49
L58	206	56 and shift\$3	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:49
L59	161	58 and logic	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:54
L60	45	58 not 59	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:55
L61	324	56 not 58	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:55
L62	0	61 and ("AND gate" or "AND logic" or "logic AND")	US-PGPUB; USPAT	OR	OFF	2006/01/31 11:56
L63	58	61 and "AND"	US-PGPUB; USPAT	OR	OFF	2006/01/31 12:02
L64	266	61 not 63	US-PGPUB; USPAT	OR	OFF	2006/01/31 12:03
L65	127	64 and capacit\$4	US-PGPUB; USPAT	OR	OFF	2006/01/31 12:05
L66	139	64 not 65	US-PGPUB; USPAT	OR	OFF	2006/01/31 12:05